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C.O.C

**TRANSMITTAL LETTER  
(General - Patent Issued)**Docket No.  
FIS920030051US1Patentee/Assignee  
Steegen et al.

U.S. Patent No.

6,936,522 B2

Issue Date

August 30, 2005

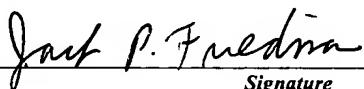
Title: **SELECTIVE SILICON-ON-INSULATOR ISOLATION STRUCTURE AND METHOD****COMMISSIONER FOR PATENTS:**

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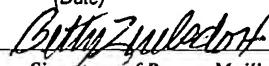
Dated: 11-28-2005

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# UNITED STATES PATENT AND TRADEMARK OFFICE

## CERTIFICATE OF CORRECTION

PATENT NO. : 6,936,522 B2

DATED : August 30, 2005

INVENTOR(S) : Steegen et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

### Column 3

Line 30, delete “Å□ thick” and insert -- Åthick --

### Column 6

Line 2, delete “700°0 C.” and insert -- 700° C. --

Lines 61 and 62, delete “Å thick” and insert --Åthick--

### Column 9

Line 44, delete “(c)” and insert --(e)--

### Column 10

Line 1, delete “front” and insert --from--

Line 20, delete “method claim” and insert --method of claim --

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PATENT NO. 6,936,522 B2

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